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Manu Kashyap, Intellectual Property Paralegal

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Re: 10/736,386

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			First Named Inventor	Bruce Whitefield		
			Group Art Unit	2123		
			Examiner Name			
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See 37 C.F.R. 1.27 and 1.28. 2123 Group Art Unit Examiner Name TOTAL AMOUNT OF PAYMENT (c.

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In re Application of :

Bruce Whitefield et al.

Š Group Art Unit: 2123 CENTRAL FAX CENTER

Serial No.:

10/736,386 December 15, 2003

Examiner:

OCT 27 2iun

Filed:

Atty Docket:

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For: Method For Calculating High-Resolution Wafer Parameter Profiles

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The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035

408-433-7475

Date: 270 005

Respectfully submitted.

125Rall

Timothy Croll

Reg. No. 36,771

NO.772 P005

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Examiner tritials*	Cite	Document Number	Publication Date	T DOCUMENTS Name of Patentee or	Pages, Columns, Lines, Where	
	Cite No.1	Number-Kind Code ^{2 (Fances)}	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
		US- 2005/0132308	06-2005	Whitefield et al.		
		US- 2003/0229410	12-2003	Smith et al.		
	1	US- 2004/0162692	08-2004	Ye et al.		
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